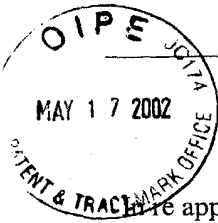


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**IN THE UNITED STATE PATENT AND TRADEMARK OFFICE**

Re application of : MOU-SHIUNG LIN et al.

Application No. : 10/055,560

Filed : January 22,2002

For : INTEGRATED CHIP PACKAGE  
STRUCTURE USING METAL SUBSTRATE  
AND METHOD OF MANUFACTURING THE  
SAME

*#4  
P. Higgins  
8-552*

Examiner :

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Transmitted herewith is a certified copy of Taiwan Application No. **90133194** filed on **December 31, 2001**.

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